

# More Loop Unrolling and Vectorization

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# Loop Unrolling Review

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```
    li r0 <- 0
    syscall IO.in_int
    li r2 <- 0
    li r3 <- 1
L1:  ble r1 r0 L2
    add r2 <- r2 r0
    add r0 <- r0 r3
    jmp L1
L2:  mov r1 <- r2
    syscall IO.out_int
```

# Loop Unrolling Review

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```
li r0 <- 0
syscall IO.in_int
li r2 <- 0
li r3 <- 1
L1: ble r1 r0 L2
    add r2 <- r2 r0
    add r0 <- r0 r3
    jmp L1
L2: mov r1 <- r2
    syscall IO.out_int
```

**Goal:** unroll this loop, without duplicating `ble`.

Unrolled loop runs for a multiple of the unrolling factor.

- `r0`, `r1`, and number of iterations determine if we have extra iterations

# Data-Flow Analysis for Affine Expressions

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Similar to constant propagation. **Meet operator:**

**Direction:** Forward

**Values:** (for each variable)

- Unknown ( $\top$ )
- Affine expression  
( $c_0 + c_1x_1 + c_2x_2 + \dots$ )
- Not affine expression ( $\perp$ )

- Let  $v[x]$  be the data-flow value for variable  $x$ .
- Usual rules for  $\top$ .
- If  $v_1[x] = v_2[x]$ :
  - $(v_1 \wedge v_2)[x] = v_1[x]$
- Otherwise,
  - $(v_1 \wedge v_2)[x] = \perp$

# Data-Flow Analysis for Affine Expressions

Statement	Transfer Function
<code>la x &lt;- c</code>	$f_s(v)[x] = c$
<code>li x &lt;- c</code>	$f_s(v)[x] = c$
<code>ld x &lt;- y[c]</code>	$f_s(v)[x] = v[y[c]]$
<code>mov x &lt;- y</code>	$f_s(v)[x] = v[y]$
<code>add x &lt;- y z</code>	$f_s(v)[x] = v[y] + v[z]$
<code>mul x &lt;- y z</code>	$f_s(v)[x] = v[y] \cdot v[z]$ (if $v[y] = c$ or $v[z] = c$ )
<code>div x &lt;- y z</code>	$f_s(v)[x] = v[y]/v[z]$ (if $v[z] = c$ and $v[z] \neq 0$ )

# Data-Flow Analysis for Affine Expressions

Statement	Transfer Function
<code>la x ← c</code>	$f_s(v)[x] = c$
<code>li x ← c</code>	$f_s(v)[x] = c$
<code>ld x ← y[c]</code>	$f_s(v)[x] = v[y[c]]$
<code>mov x ← y</code>	$f_s(v)[x] = v[y]$
<code>add x ← y z</code>	$f_s(v)[x] = v[y] + v[z]$
<code>mul x ← y z</code>	$f_s(v)[x] = v[y] \cdot v[z]$ (if $v[y] = c$ or $v[z] = c$ )
<code>div x ← y z</code>	$f_s(v)[x] = v[y]/v[z]$ (if $v[z] = c$ and $v[z] \neq 0$ )

$v[y] = \perp$   
or  
 $v[z] = \perp$  }  $\Rightarrow f_s(v)[x] = \perp$

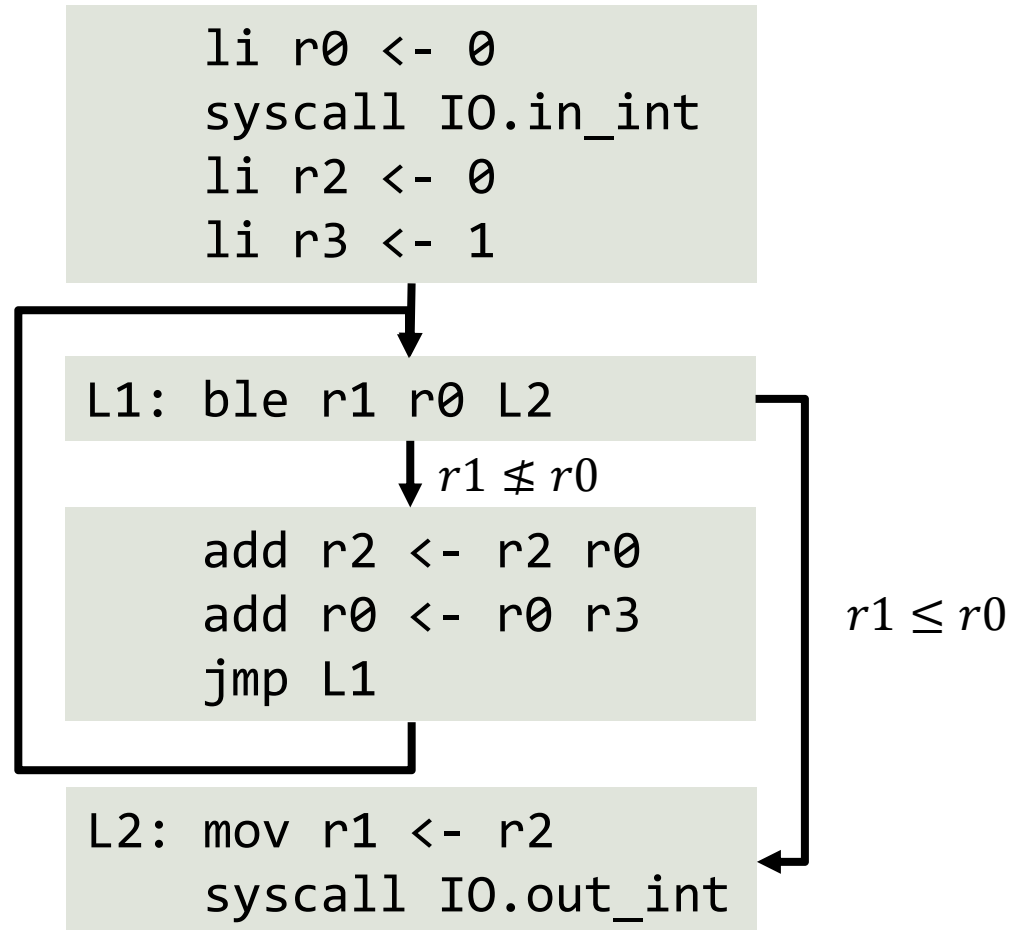
# Loop Example

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```
li r0 <- 0
syscall IO.in_int
li r2 <- 0
li r3 <- 1
L1: ble r1 r0 L2
    add r2 <- r2 r0
    add r0 <- r0 r3
    jmp L1
L2: mov r1 <- r2
    syscall IO.out_int
```

# Loop Example (CFG)

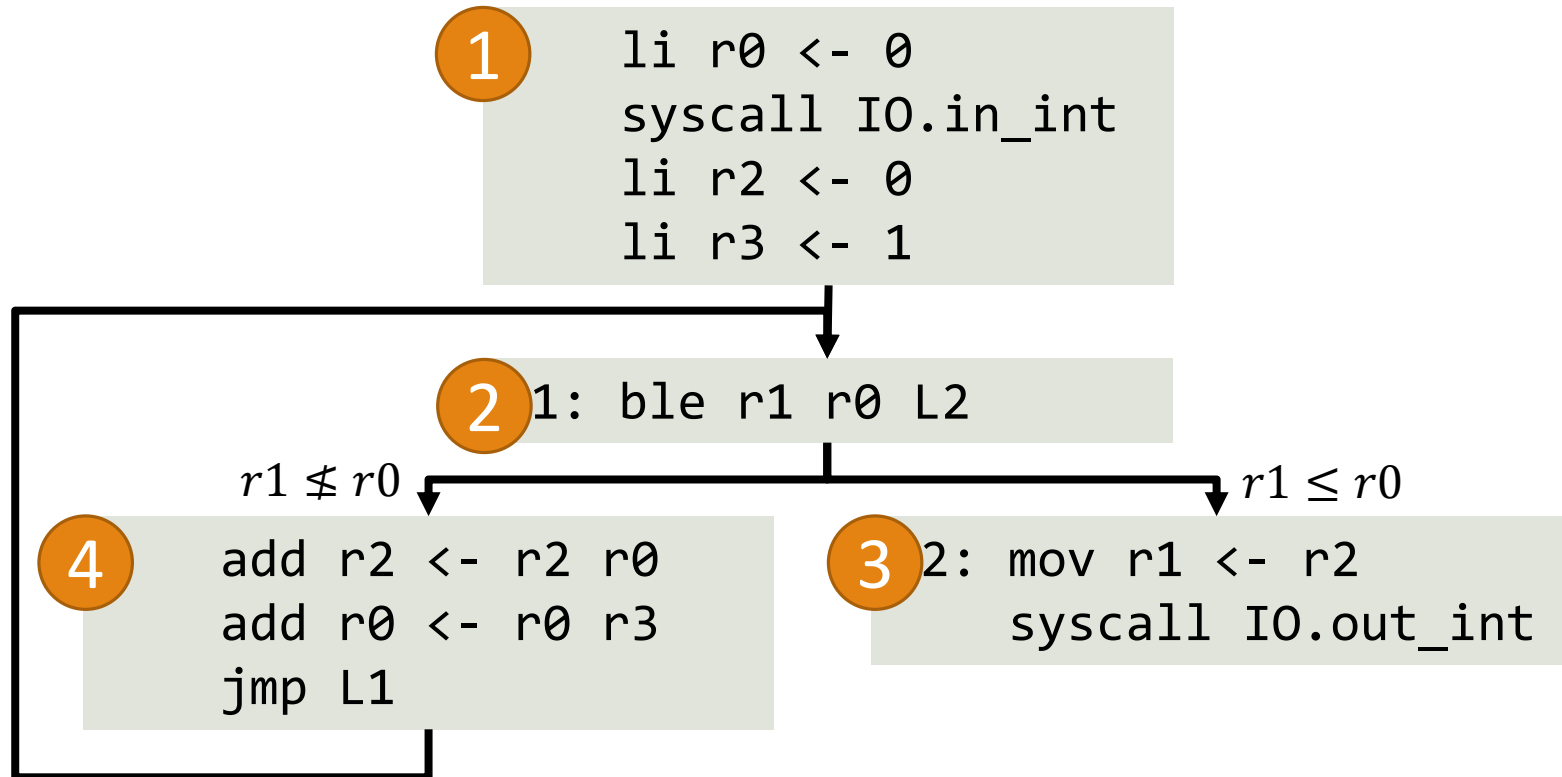
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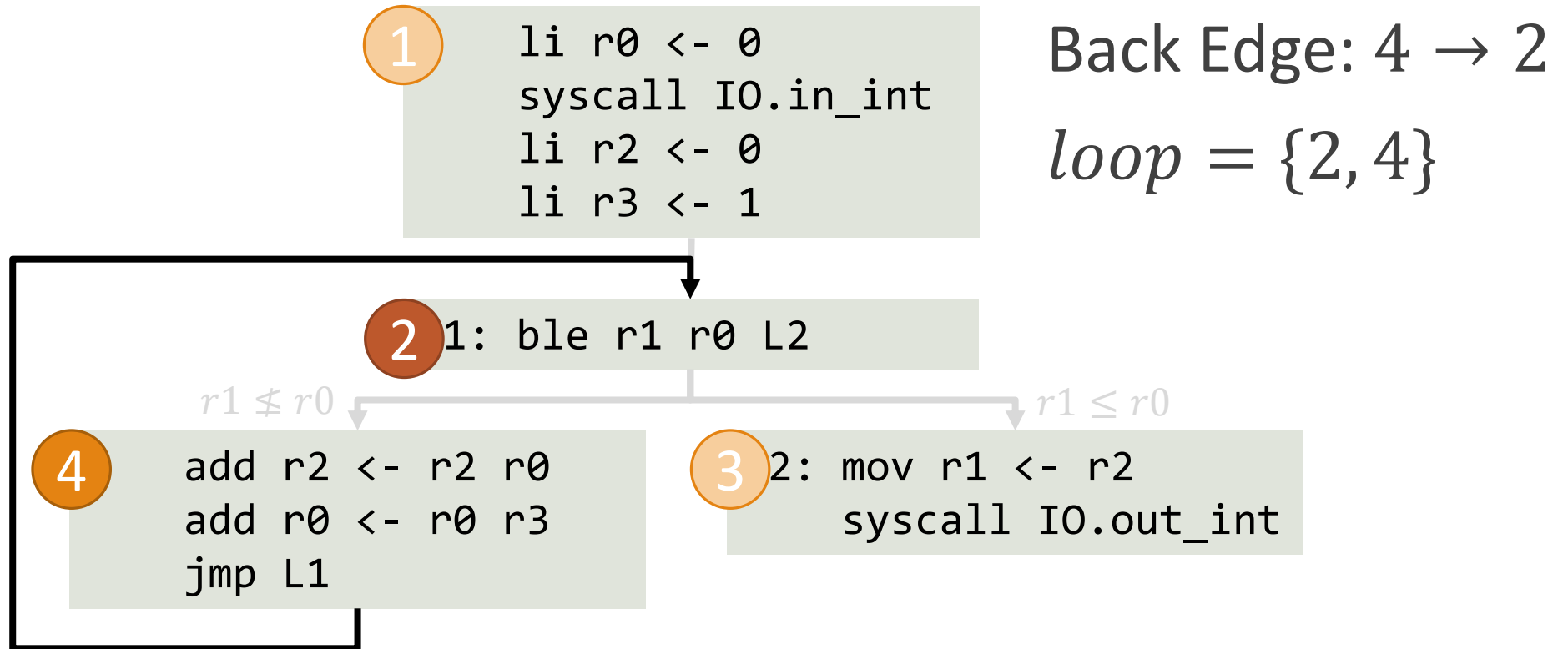


# Loop Example (DFS Tree)

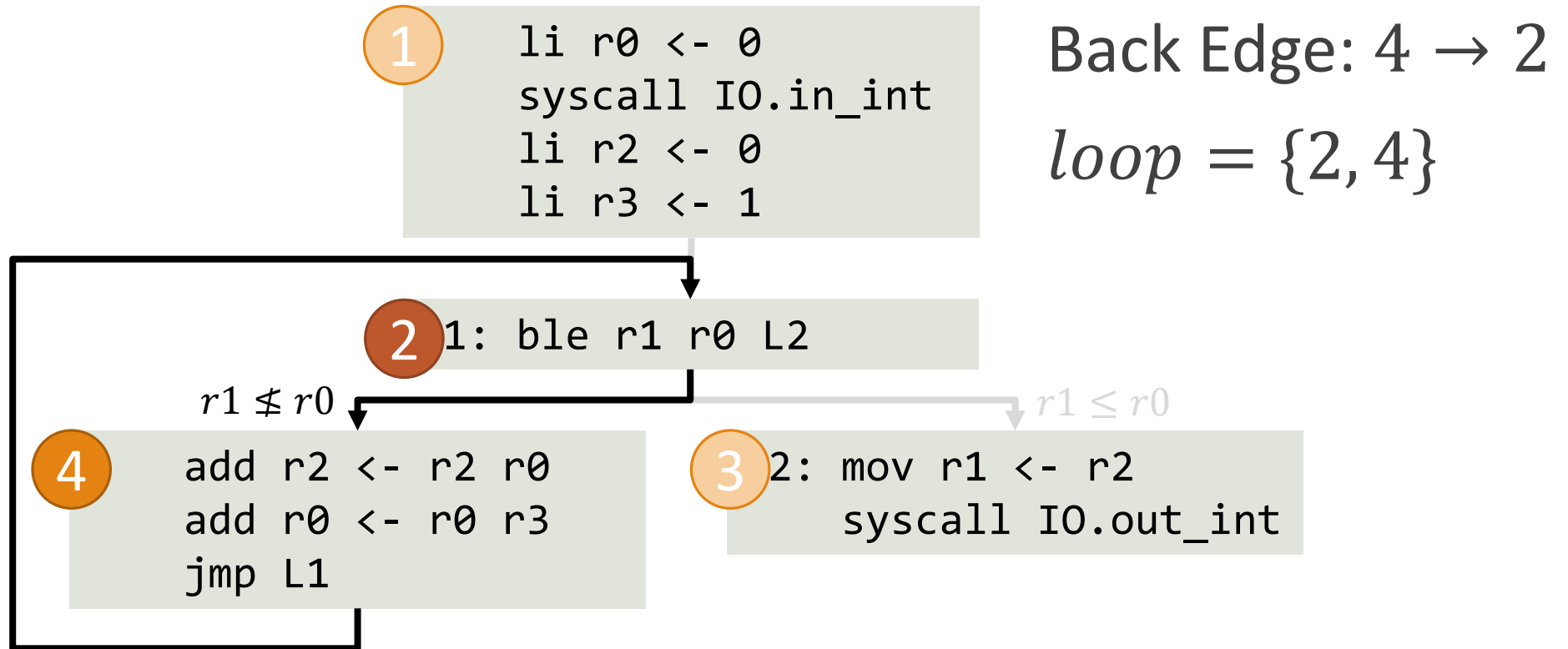
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# Loop Example (Loop Detection)

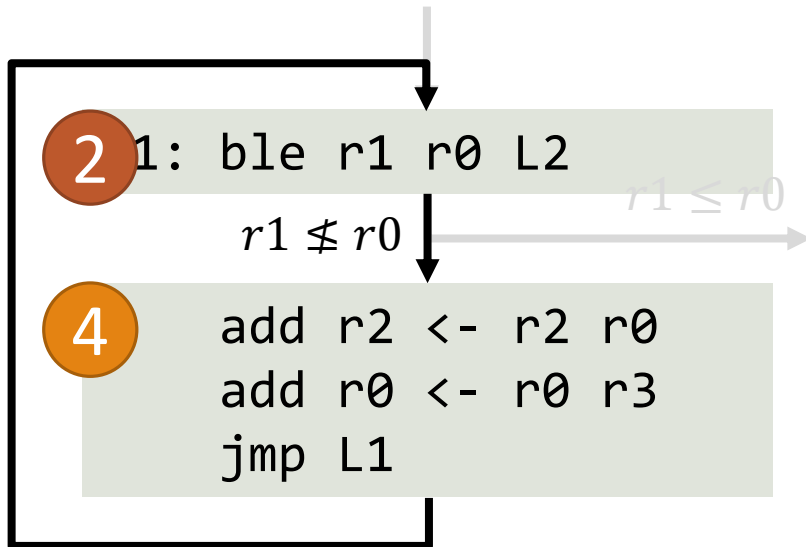


# Loop Example (Loop Detection)



# Loop Example (Data-Flow Analysis)

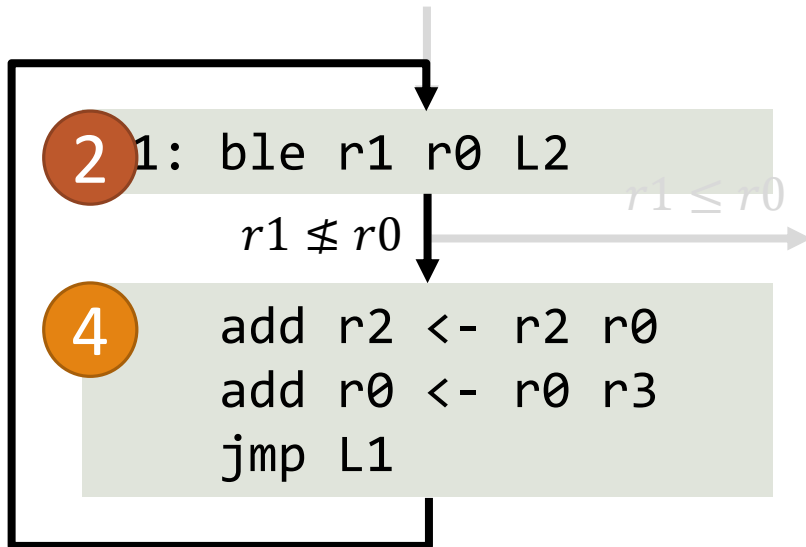
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Var	$f_{B_2}(v)$	$f_{B_4}(v)$
r0		
r1		
r2		
r3		

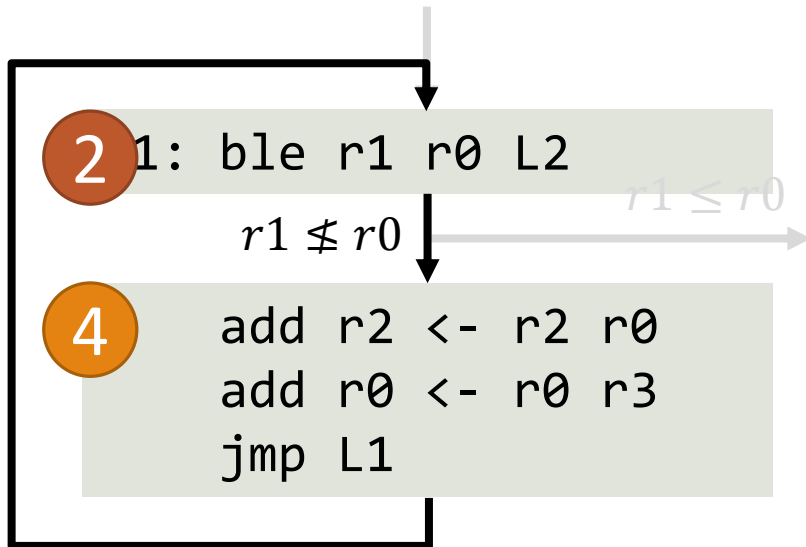
# Loop Example (Data-Flow Analysis)

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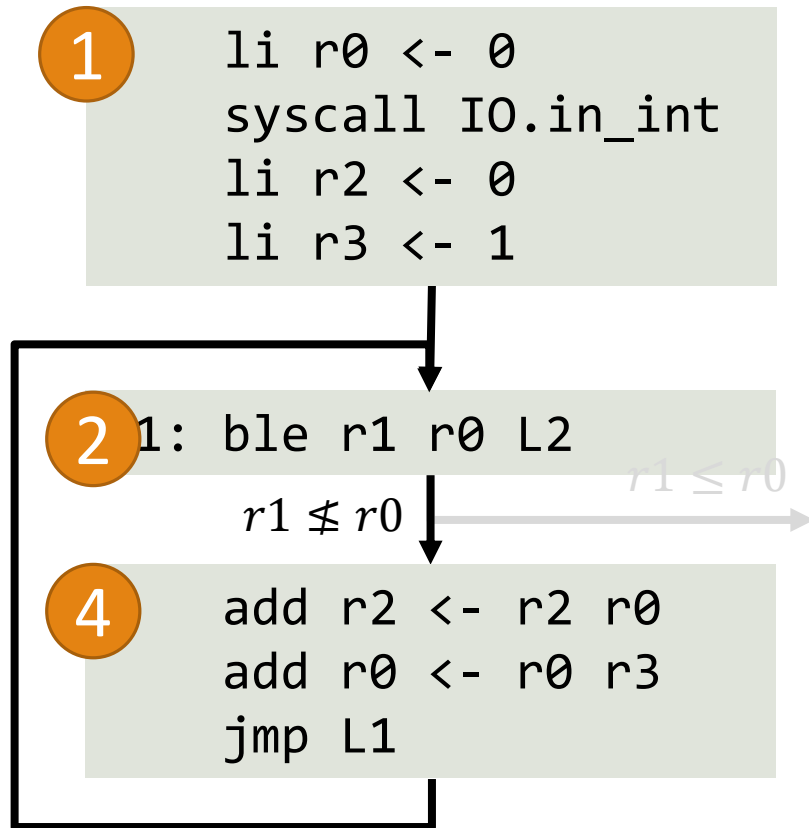
Var	$f_{B_2}(v)$	$f_{B_4}(v)$
r0	$v[r0]$	
r1	$v[r1]$	
r2	$v[r2]$	
r3	$v[r3]$	

# Loop Example (Data-Flow Analysis)



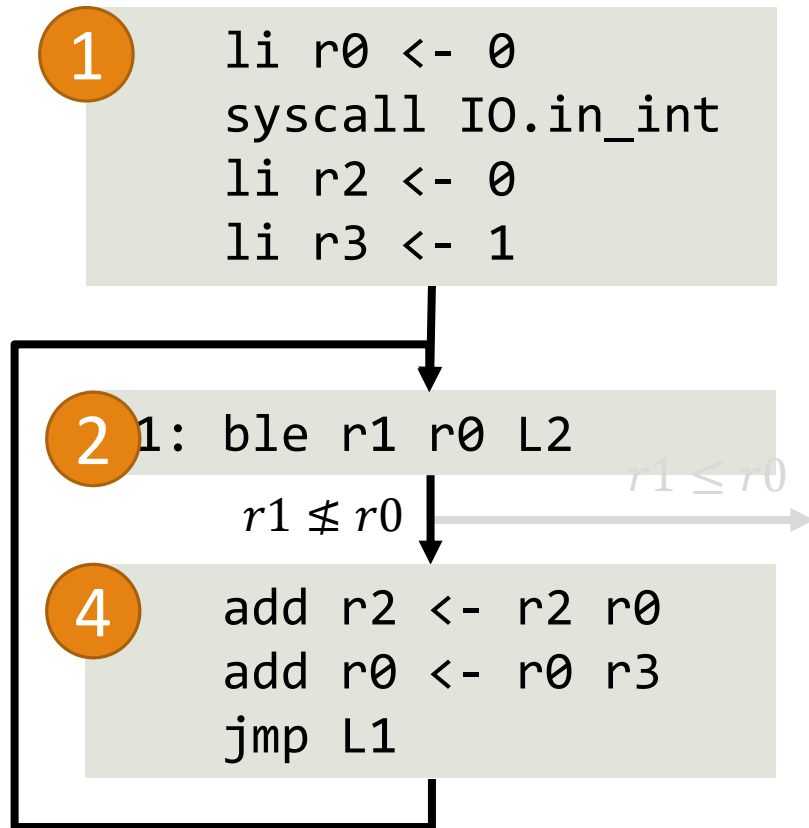
Var	$f_{B_2}(v)$	$f_{B_4}(v)$
r0	$v[r0]$	$v[r0] + v[r3]$
r1	$v[r1]$	$v[r1]$
r2	$v[r2]$	$v[r2] + v[r0]$
r3	$v[r3]$	$v[r3]$

# Loop Example (Data-Flow Analysis)



Var	IN[B2]	OUT[B4]
r0		
r1		
r2		
r3		

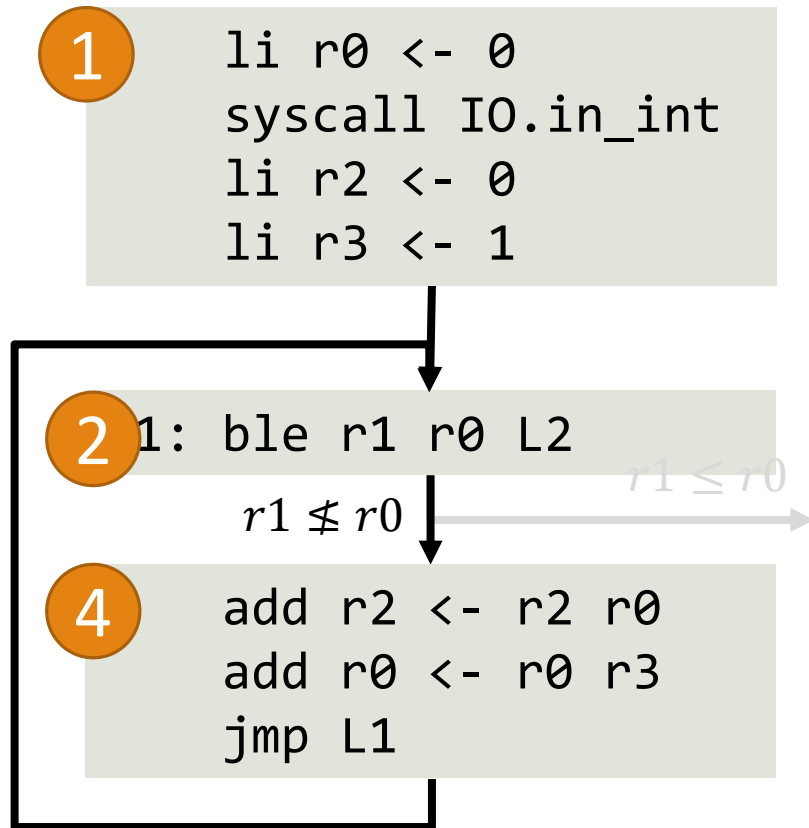
# Loop Example (Data-Flow Analysis)



Var	IN[B2]	OUT[B4]
r0	$0 \wedge T = 0$	1
r1	$\perp \wedge T = \perp$	$\perp$
r2	$0 \wedge T = 0$	0
r3	$1 \wedge T = 1$	1

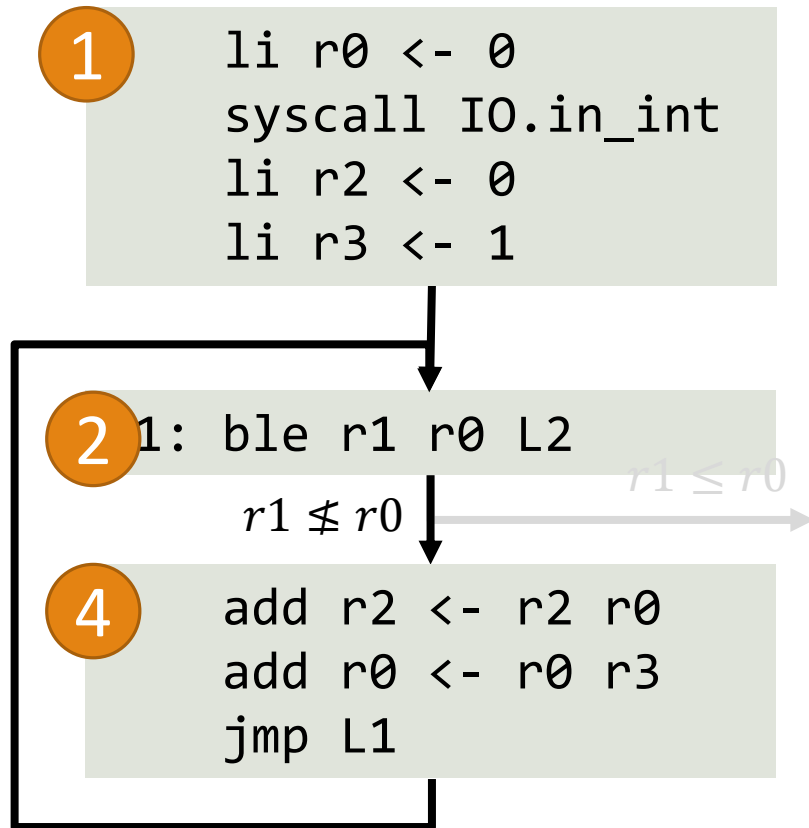


# Loop Example (Data-Flow Analysis)



Var	IN[B2]	OUT[B4]
r0	$0 \wedge 1 = \perp$	$\perp$
r1	$\perp \wedge \perp = \perp$	$\perp$
r2	$0 \wedge 0 = 0$	$\perp$
r3	$1 \wedge 1 = 1$	1

# Loop Example (Data-Flow Analysis)



Total failure!

Var		OUT[B4]
r0	$0 \wedge \perp = \perp$	$\perp$
r1	$\perp \wedge \perp = \perp$	$\perp$
r2	$0 \wedge \perp = \perp$	$\perp$
r3	$1 \wedge 1 = 1$	1

# Iterated Transfer Functions

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Track data-flow values as functions of number of iterations.

- After 1 iteration:

$$f_{B_4}^1(v_0)[r0] = v_0[r0] + v_0[r3] = v_0[r0] + 1$$

- After 2 iterations:

$$f_{B_4}^2(v_0)[r0] = (v_0[r0] + 1) + 1 = v_0[r0] + 2$$

- After  $i$  iterations:

$$f_{B_4}^i(v_0)[r0] = v_0[r0] + i$$

# Handling Iteration

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## **Symbolic constants:**

- If  $f(v)[x] = v[x]$ ,  $f^i(v_0)[x] = v_0[x]$

## **Basic induction variables:**

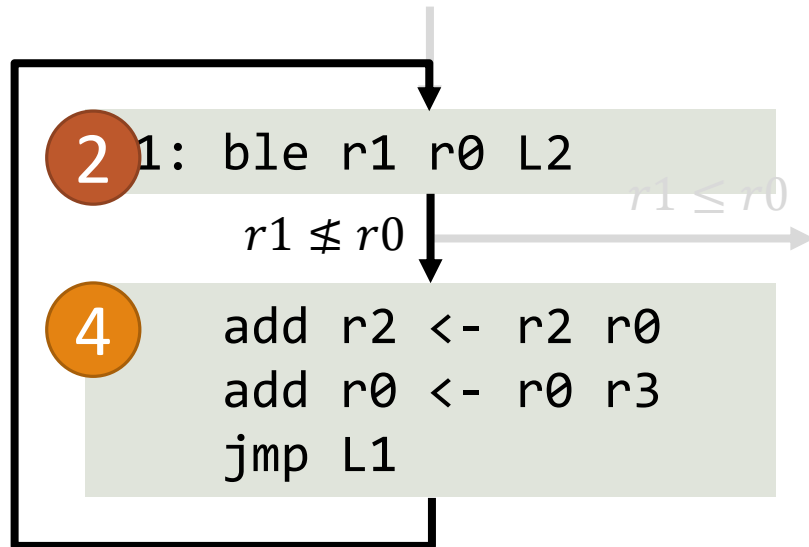
- If  $f(v)[x] = c + v[x]$ ,  $f^i(v_0)[x] = ci + v_0[x]$

**Induction variables** (if  $y_1 \dots$  are basic induction variables or symbolic constants and  $x \not\equiv y_i$ ):

- If  $f(v)[x] = c_0 + c_1v[y_1] + \dots$ ,  $f^i(v_0)[x] = c_0 + c_1f^i(v_0)[y_1] + \dots$

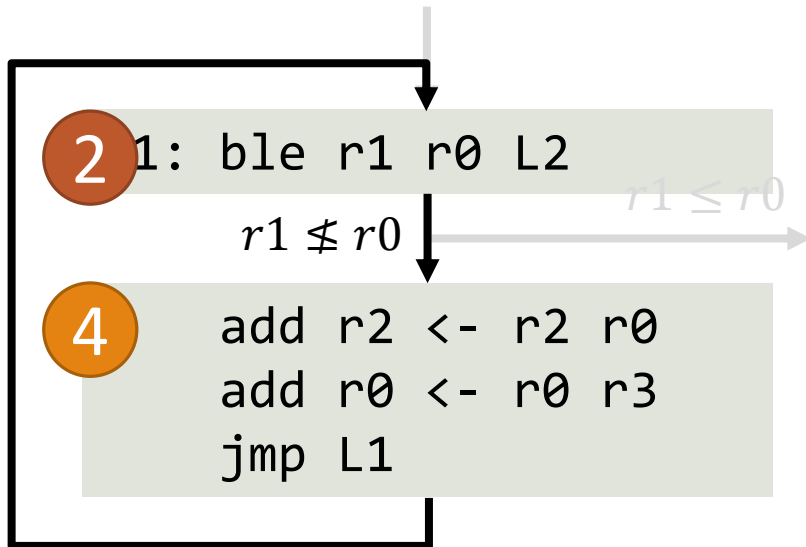
# Loop Example (Data-Flow Analysis)

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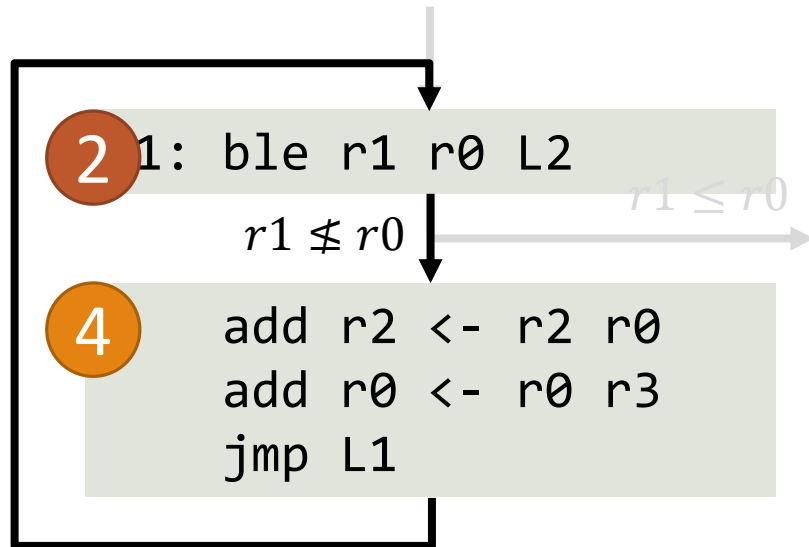
Var	$f_{B_4}(v)$	$f_{B_4}^i(v_0)$
r0	$v[r0] + v[r3]$	
r1	$v[r1]$	
r2	$v[r2] + v[r0]$	
r3	$v[r3]$	

# Loop Example (Data-Flow Analysis)



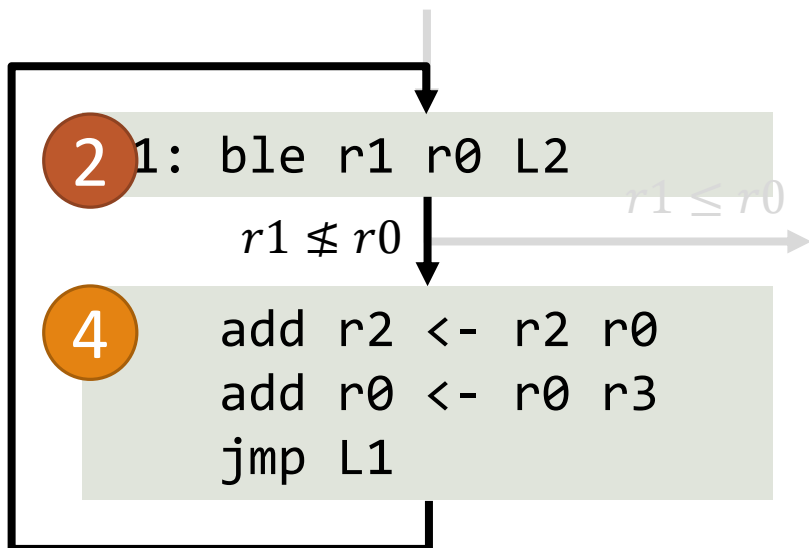
Var	$f_{B_4}(v)$	$f_{B_4}^i(v_0)$
r0	$v[r0] + v[r3]$	
r1	$v[r1]$	$v_0[r1]$
r2	$v[r2] + v[r0]$	
r3	$v[r3]$	$v_0[r3]$

# Loop Example (Data-Flow Analysis)



Var	$f_{B_4}(v)$	$f_{B_4}^i(v_0)$
r0	$v[r0] + v[r3]$	$v_0[r0] + v_0[r3]i$
r1	$v[r1]$	$v_0[r1]$
r2	$v[r2] + v[r0]$	
r3	$v[r3]$	$v_0[r3]$

# Loop Example (Data-Flow Analysis)



Var	$f_{B_4}(v)$	$f_{B_4}^i(v_0)$
r0	$v[r0] + v[r3]$	$v_0[r0] + v_0[r3]i$
r1	$v[r1]$	$v_0[r1]$
r2	$v[r2] + v[r0]$	$\perp$
r3	$v[r3]$	$v_0[r3]$



# Finding the Number of Iterations

---

Use  $f^i$  to compute value on back edges.

We want to find  $i_{max}$  such that:

$$f^i(v_0)[r1] \not\leq f^i(v_0)[r0]$$

# Finding the Number of Iterations

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Use  $f^i$  to compute value on back edges.

We want to find  $i_{max}$  such that:

$$f^i(v_0)[r1] \not\leq f^i(v_0)[r0]$$

$$v_0[r1] \not\leq v_0[r0] + v_0[r3]i_{max}$$

$$\frac{v_0[r1] - v_0[r0]}{v_0[r3]} > i_{max}$$

$$v_0[r1] = v_0[r0] + v_0[r3](i_{max} + 1)$$

# Loop Unrolling

---

```
li r0 <- 0
syscall IO.in_int
li r2 <- 0
li r3 <- 1
L1: ble r1 r0 L2
    add r2 <- r2 r0
    add r0 <- r0 r3
    jmp L1
L2: mov r1 <- r2
    syscall IO.out_int
```

Now we know initial value of **r1** sets number of iterations.

- Check it against the loop unrolling factor to handle extra iterations.

# Loop Unrolling

```
li r0 <- 0
syscall IO.in_int
li r2 <- 0
li r3 <- 1
```

```
li r4 <- 3; factor
```

```
div r5 <- r1 r4
```

```
mul r5 <- r5 r4
```

```
sub r5 <- r1 r5
```

```
bz r5 L1
```

```
add r2 <- r2 r0
```

```
add r0 <- r0 r3
```

```
beq r5 r0 L1
```

```
add r2 <- r2 r0
```

```
add r0 <- r0 r3
```

```
L1: beq r1 r0 L2
```

Unrolling factor

$r5 \leftarrow r1 \bmod r4$

Handle extra iterations.

# Auto-Vectorization

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# Automatic Vectorization

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Similar to loop unrolling:

- Consecutive iterations with *independent* arithmetic.
- Perform arithmetic for several iterations together in vector.
- Usually implemented over arrays.

```
let x : List <- getlist() in
while not isvoid(x) loop {
  x.incrBy(2);
  x <- x.next();
} pool
```

# Automatic Vectorization

---

Similar to loop unrolling:

- Consecutive iterations with *independent* arithmetic.
- Perform arithmetic for several iterations together in vector.
- Usually implemented over arrays.

```
let x : List<int> = ... in
while not isEmpty(x) loop {
  x.incrBy(2);
  x <- x.next();
} pool
```

Inline these.

# Automatic Vectorization

---

Similar to loop unrolling:

- Consecutive iterations with *independent* arithmetic.
- Perform arithmetic for several iterations together in vector.
- Usually implemented over arrays.

Unroll this.

```
let x : List <- getlist() in
while not isvoid(x) loop {
  x.incrBy(2);
  x <- x.next();
} pool
```



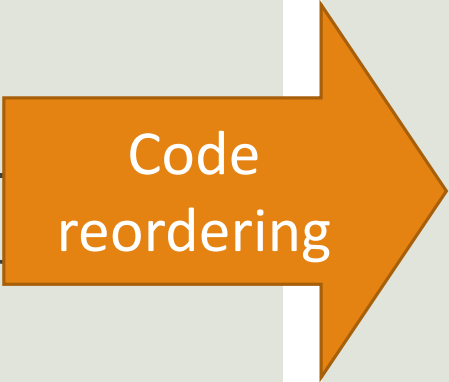
# Automatic Vectorization (Cool ASM)

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```
    li t1 <- 2
L1:  bz r0 L2
    ld t2 <- r0[3] ; x.incrby(2)
    add t3 <- t2 t1
    st r0[3] <- t3
    ld t4 <- r0[4] ; x<-x.next()
    ld t5 <- t4[3] ; x.incrby(2)
    add t6 <- t5 t1
    st t4[3] <- t6
    ld r0 <- t4[4] ; x<-x.next()
    jmp L1
```

# Automatic Vectorization (Cool ASM)

```
li t1 <- 2
L1: bz r0 L2
ld t2 <- r0[3] ; x.incrby(2)
add t3 <- t2 t1
st r0[3] <- t3
ld t4 <- r0[4] ; x<-x.incrby(2)
ld t5 <- t4[3] ; x.incrby(2)
add t6 <- t5 t1
st t4[3] <- t6
ld r0 <- t4[4] ; x<-x.next()
jmp L1
```



```
li t1 <- 2
L1: bz r0 L2
ld t4 <- r0[4]
ld t2 <- r0[3]
ld t5 <- t4[3]
add t3 <- t2 t1
add t6 <- t5 t1
st r0[3] <- t3
st t4[3] <- t6
ld r0 <- t4[4]
jmp L1
```

# Automatic Vectorization (Cool ASM)

---

1. Group arithmetic together.
2. Pack temporaries in vector registers.
3. Replace add with vector-add.
4. Unpack vector result.

```
    li t1 <- 2
L1:  bz r0 L2
     ld t4 <- r0[4]
     ld t2 <- r0[3]
     ld t5 <- t4[3]
     add t3 <- t2 t1
     add t6 <- t5 t1
     st r0[3] <- t3
     st t4[3] <- t6
     ld r0 <- t4[4]
     jmp L1
```

# Automatic Vectorization (Cool ASM)

---

1. Group arithmetic together.
2. Pack temporaries in vector registers.
3. Replace add with vector-add.
4. Unpack vector result.

```
    li vr10 <- 2
    li vr11 <- 2
L1:  bz r0 L2
    ld t4 <- r0[4]
    ld vr00 <- r0[3]
    ld vr01 <- t4[3]
    vadd vr0 <- vr0 vr1
    st r0[3] <- vr00
    st t4[3] <- vr01
    ld r0 <- t4[4]
    jmp L1
```

# A Simple Interprocedural Analysis

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# A Simple Interprocedural Analysis

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Idea: Treat method calls as control flow.

If method instance is known:

- Add CFG edge from call to top of method body.
- Add CFG edge from end of method to statement-after-call.
- Similar to inlining, but without the code bloat.

Extension: “clone” method’s CFG nodes for each invocation.

***This analysis has difficulty with recursion.***

# Interprocedural Example

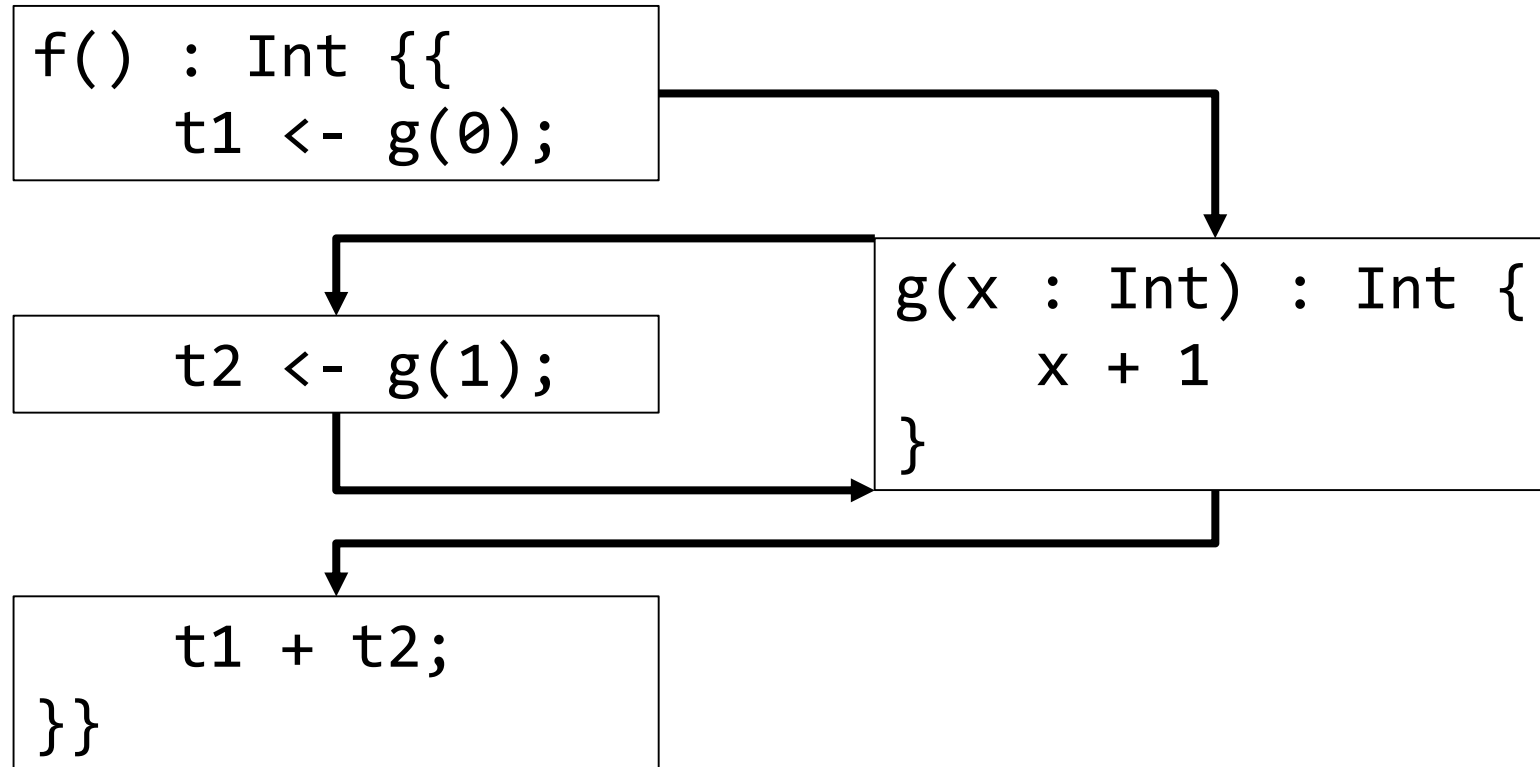
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```
f() : Int {{  
    t1 <- g(0);  
    t2 <- g(1);  
    t1 + t2;  
}}
```

```
g(x : Int) : Int {  
    x + 1  
}
```

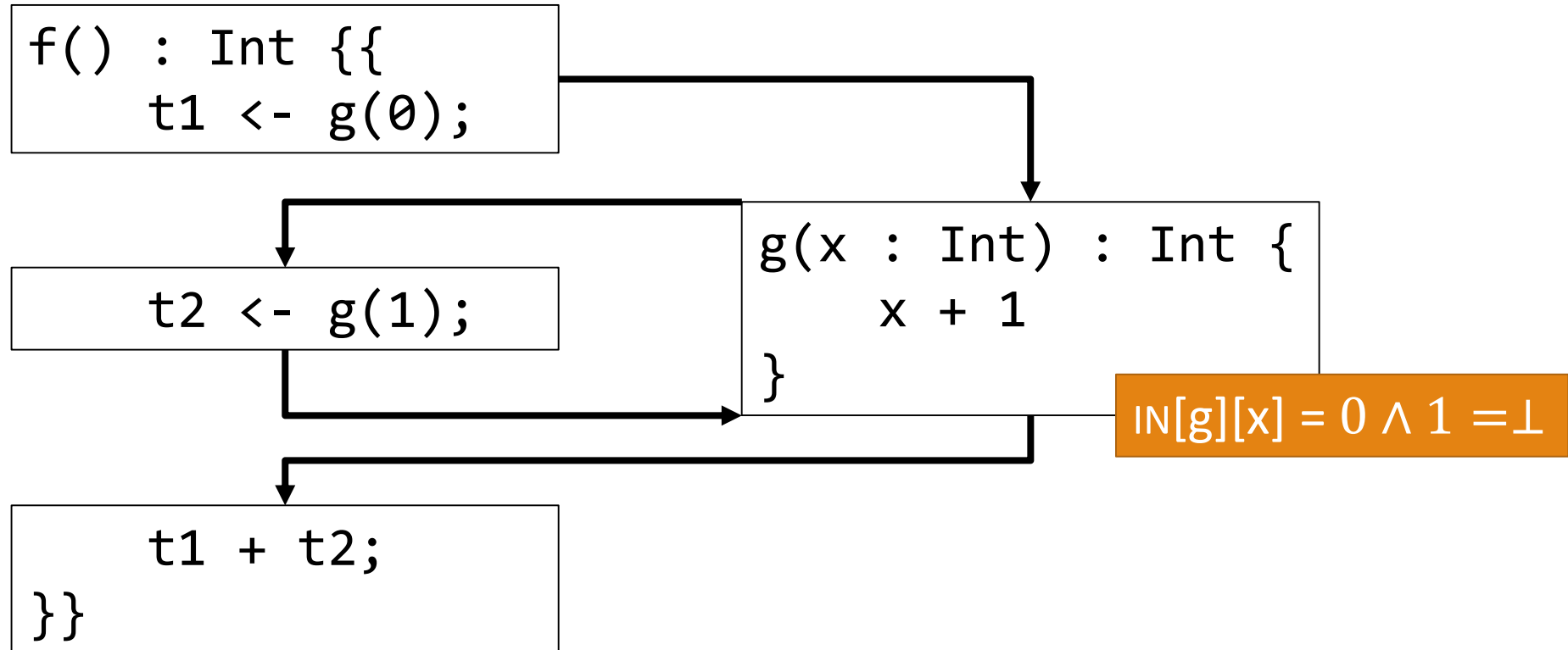
# Interprocedural Example

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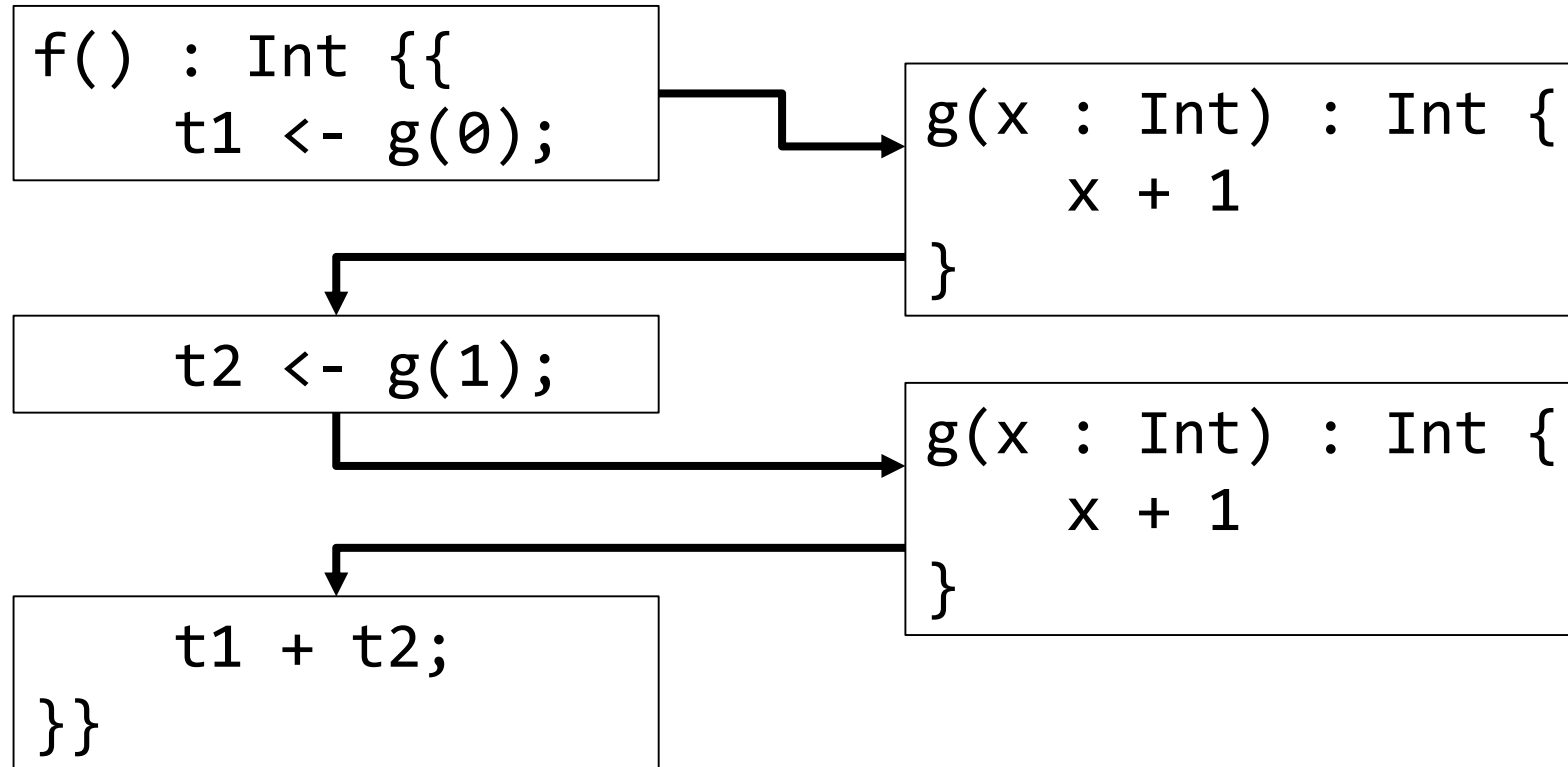


# Interprocedural Example



# Interprocedural Example

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# Interprocedural Example

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